\perp				
į		Application No.	Applicant(s)	
	Motion of Allowshility	10/767,265	DERVISOGLU ET AL.	
	Notice of Allowability	Examiner	Art Unit	
		David Ton	2133	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.				
1. This communication is responsive to <u>Application filed 1/30/2004</u> .				
2. The allowed claim(s) is/are <u>1-6</u> .				
3. The drawings filed on 30 January 2004 are accepted by the Examiner.				
A n T 5. [4.			
1. [2 2. [3. [achment(s) Notice of References Cited (PTO-892) Notice of Draftperson's Patent Drawing Review (PTO-948) Information Disclosure Statements (PTO-1449 or PTO/SB/0-Paper No./Mail Date 1/30/04 Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview Summary Paper No./Mail Dat 8), 7. Examiner's Amendr 8. Examiner's Stateme 9. Other DAVI	te ment/Comment ent of Reasons for Allowance	

w

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04)

Application/Control Number: 10/767,265

Art Unit: 2133

Examiner Amendment

- 1. An Examiner's Amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 C.F.R. 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the Issue Fee.
- a). Specification, page 1, paragraph [0001], line 2, after "09/275,726", insert --, now U.S. patent no. 6,687,865--.
 - b). Claim 2, line 10, delete the second occurrence of period ".".

Allowance

- 2. Claims 1-6 are allowed.
- 3. The following is an Examiner's Statement of Reasons for Allowance:
- a). The prior art of record teaches the claimed invention substantially, but it fails to teach or suggest singly or in combination an I/O port connector comprising a storage element, a probe in port, a probe out port, a data in port, a data out port, a scan in port and a scan out port wherein, in first mode of operation, contents of said storage element are used to select between data on said data in port and said probe in port to

Page 3

Application/Control Number: 10/767,265

Art Unit: 2133

propagate directly to said probe out port as set forth in independent claim 1. Claims 1-4

are allowed because of the combination of additional limitations and the limitation listed

above.

b). The prior art of record teaches the claimed invention substantially, but it

fails to teach or suggest singly or in combination an integrated circuit comprising a

multiplicity of logic blocks, an on-chip logic analyzer with a multiplicity of input ports and

a multiplicity of probe lines wherein each of said probe lines is adapted to capture

signals from said logic blocks and to propagate said signals to one of said multiplicity of

input ports of said on-chip logic analyzer and said input ports of said on-chip logic

analyzer comprising means to capture said signals from said probe lines, means to

align said signals propagated through said probe lines to create aligned signals and

means to capture said aligned signals as set forth in independent claim 5.

4. Any comments considered necessary by applicant must be submitted no later

than the payment of the Issue Fee and, to avoid processing delays, should preferably

accompany the Issue Fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance".

Conclusion



Application/Control Number: 10/767,265

Art Unit: 2133

- 5. The prior art of record and not relied upon is considered pertinent to applicant's disclosure.
- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Ton whose telephone number is (571) 272-3828. The examiner can normally be reached on M-Th from 5:30 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert DeCady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

David Ton
Primary Exami

Primary Examiner
Art Unit 2133